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VLSI Objective Questions and answers

- 1) Chip utilization depends on ____
 - a. Only on standard cells
 - b. Standard cells and macros
 - c. Only on macros
 - d. Standard cells macros and IO pads
- 2) In SDRAM blockages ____ cells are placed.
 - a. Only peripheral cells
 - b. Top cells
 - c. Only buffers and inverters
 - d. Any cells
- 3) Why we have to remove scan chains before placement?
 - a. Because scan chains are group of flip flop
 - b. It does not have strong critical path
 - c. It is series of flip flop connected in FIFO
 - d. None
- 4) Delay between shortest path and longest path in the clock is called ____
 - a. Useful skew
 - b. Local skew
 - c. Global skew
 - d. Slack
- 5) Cross talk can be avoided by ____
 - a. Decreasing the spacing between the metal layers
 - b. Doubling the metal
 - c. Using lower metal layers
 - d. Using long nets
- 6) Pre-routing means routing of ____
 - a. Clock nets
 - b. Signal nets
 - c. IO nets
 - d. Bus nets
- 7) Which of the following metal layer has Maximum resistance?
 - a. Metal1
 - b. Metal2
 - c. Metal3
 - d. Metal4
- 8) What is the goal of CTS?
 - a. Clock tree synthesis
 - b. Clock tree synthesis
 - c. Clock tree synthesis
 - d. Clock tree synthesis

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